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EXAMINER	
KENNEDY, JENNIFER M	

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Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/053,572	Applicant(s) OHNUMA, HIDETO	
	Examiner Jennifer M. Kennedy	Art Unit 2812	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *21 June 2004*.

2a)☒ This action is **FINAL**. 2b)☐ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-36* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-36* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☒ All b)☐ Some * c)☐ None of:

1.☒ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☐ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____.
5) ☐ Notice of Informal Patent Application (PTO-152)
6) ☐ Other: _____.

DETAILED ACTION

Response to Amendment

In view of Applicant's amendment to the claims, the objections of claims 2-3, and 29-30 are withdrawn.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

> (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-36 are rejected under 35 U.S.C. 103(a) as being unpatentable over Zhang et al. (U.S. Patent Appl 2002/0098635) in view of Ohtani et al. (U.S. Patent No. 5,966,596).

In re claim 1, Zhang et al. discloses the method of forming a semiconductor film comprising silicon (31) over an insulating substrate (1), forming an oxide film (33) on a surface of the semiconductor film comprising silicon, doping the semiconductor film comprising silicon with impurity ions through the oxide film (see [0118]-[0119]) and forming at least one channel region comprising a portion of the doped semiconductor film (see [0121]-[0124]).

Zhang et al. does not disclose the method of forming a chemical oxide film. The examiner notes that Zhang et al. does not disclose a particular method for forming the

oxide layer, 33, and therefore the particular method used to form the oxide layer lacks criticality in the invention of Zhang et al. One of ordinary skill in the art at the time the invention was made would have recognized that any known method could be used to form the oxide layer, 33, in the absence of a particular suggestion by Zhang et al. Ohtani et al. discloses a method of forming a chemical oxide film (see column 2, lines 44-46, and column 6, lines 55-64). It would have been obvious to one of ordinary skill in the art at the time the invention was made to form the oxide layer of Zhang et al. by the method of Ohtani et al., since the method of forming an oxide layer lacks criticality in the invention of Zhang et al., and since the method of Ohtani is a known method of forming an oxide that improves the surface characteristics of the underlying film (see Ohtani et al. column 2, lines 39-46).

In re claim 4, Zhang et al. teaches the method wherein the semiconductor film comprising silicon is an amorphous semiconductor film comprising silicon (31, see [0117]).

In re claims 7, 10, and 22, Zhang et al does not disclose the method wherein a catalytic element of Ni, having the effect of accelerating crystallization is applied to the amorphous semiconductor film, and a heat treatment is conducted to form a crystalline semiconductor film. Ohtani et al. also discloses the method wherein a catalytic element of Ni, having the effect of accelerating crystallization is applied to the amorphous semiconductor film, and a heat treatment is conducted to form a crystalline semiconductor film (see column 7, lines 20-50). It would have been obvious to one of ordinary skill in the art at the time the invention was made to apply a catalytic element of

Ni to the amorphous silicon film of Zhang et al. in order to accelerate the crystallization of the amorphous silicon film, thereby increasing throughput.

In re claim 13, Zhang et al. teaches the method wherein the material including hydrogen is used as the ion source for the impurity ions (see [0118]-[0119]).

In re claim 16, Zhang et al. teaches the method wherein the doping step allows channel doping to be implemented (see [0124]). The examiner notes that the doping step of ([0118]-[0119]) creates a doped silicon layer 34, which is subsequently doped and renumbered 35 and 36 (see Figure 8A-8D). The doped silicon layer is then etched into island-like semiconductor layers 11 and 12 (see [0121]), of which a portion of each are the channel region.

In re claims 19, 20, and 21, Ohtani et al. discloses the chemical oxide film is formed by treatment with ozone water, hydrogen peroxide or by ozone treatment through ultraviolet irradiation in an atmosphere containing oxygen (see column 2, lines 44-46).

In re claim 24, Zhang et al does not disclose the method wherein the semiconductor device is at least one device selected from the group consisting of personal computer, video camera, a mobile computer, a goggle type display device, a DVD player, a CD player, a portable telephone, a projector. Ohtani et al. also discloses that the semiconductor device could be a mobile computer (see column 1, lines 24-25). It would have been obvious to one of ordinary skill in the art at the time the invention was made to utilize the TFT of Zhang et al. in a mobile computer since TFTs allow for a display with high information content at a high speed.

In re claim 2, Zhang et al. discloses the method of forming a semiconductor film comprising silicon (31) over an insulating substrate (1), forming an oxide (33) film which protects the semiconductor film from being etched by a subsequent doping step (33) on a surface of the semiconductor film comprising silicon, doping the semiconductor film comprising silicon with impurity ions through the oxide film (see [0118]-[0119]) and forming at least one channel region comprising a portion of the doped semiconductor film (see [0121]-[0124]).

The examiner notes that the protective oxide layer, 33, of Zhang et al. is formed directly prior to the doping process of Zhang et al. and therefore is formed to protect during the doping process. Further, Zhang et al. teaches that the acceleration voltage of the dopant is adjusted in accordance with the thickness of the protective oxide (see [0118]). The protective oxide layer of Zhang et al. will to some degree prevent etching of the surface of the semiconductor film when compared with a semiconductor film without the protective oxide formed thereon.

Zhang et al. does not disclose the method of terminating the dangling bonds on a surface of the semiconductor film with oxygen. Ohtani et al. discloses a method of forming a chemical oxide film by ultraviolet radiation within an oxygen ambient or immersing the substrate in ozone water or hydrogen peroxide water (see column 2, lines 39-46 and column 6, lines 55-64).

Ohtani et al. does not explicitly state that the pretreatment terminates dangling bonds on a surface of the semiconductor film with oxygen. However, as explained in the applicant's specification the termination of bonds in the present application occur with oxygen (see specification, page 17, line 13 through page 18, line 5). The examiner points out that the amorphous silicon of Ohtani et al. is oxidized by illuminating the substrate with a UV light in an oxygen ambient to form the chemical oxide. The examiner notes that UV light increases the reactivity of the oxygen atoms, and it is clear by the formation of the oxide that the oxygen molecules react on the surface of the substrate, thus terminating the dangling bonds on the surface of the semiconductor film with oxygen. Finally the examiner notes that Ohtani et al. discloses the same conditions and steps as that of the applicant's disclosure with respect to forming the oxide. Applicant has not provided any required conditions for forming the oxide that would terminate the dangling bonds other than the presence of oxygen.

The examiner notes that Zhang et al. does not disclose a particular method for forming the oxide layer, 33, and therefore the particular method used to form the oxide layer lacks criticality in the invention of Zhang et al. One of ordinary skill in the art at the time the invention was made would have recognized that any known method could be used to form the oxide layer, 33, in the absence of a particular suggestion by Zhang et al. Ohtani et al. discloses a method of forming a chemical oxide film (see column 2, lines 44-46, and column 6, lines 55-64) that terminates dangling bonds with oxygen as explained above. It would have been obvious to one of ordinary skill in the art at the time the invention was made to form the oxide layer of Zhang et al. by the method of

Ohtani et al., since the method of forming an oxide layer lacks criticality in the invention of Zhang et al., and since the method of Ohtani is a known method of forming an oxide that improves the surface characteristics of the underlying film (see Ohtani et al. column 2, lines 39-46).

In re claim 5, Zhang et al. teaches the method wherein the semiconductor film comprising silicon is an amorphous semiconductor film comprising silicon (31, see [0117]).

In re claims 8 and 11, Zhang et al does not disclose the method wherein a catalytic element of Ni, having the effect of accelerating crystallization is applied to the amorphous semiconductor film, and a heat treatment is conducted to form a crystalline semiconductor film. Ohtani et al. also discloses the method wherein a catalytic element of Ni, having the effect of accelerating crystallization is applied to the amorphous semiconductor film, and a heat treatment is conducted to form a crystalline semiconductor film (see column 7, lines 20-50). It would have been obvious to one of ordinary skill in the art at the time the invention was made to apply a catalytic element of Ni to the amorphous silicon film of Zhang et al. in order to accelerate the crystallization of the amorphous silicon film, thereby increasing throughput.

In re claim 14, Zhang et al. teaches the method wherein the material including hydrogen is used as the ion source for the impurity ions (see [0118]-[0119]).

In re claim 17, Zhang et al. teaches the method wherein the doping step allows channel doping to be implemented (see [0124]). The examiner notes that the doping step of ([0118-0119]) creates a doped silicon layer 34, which is subsequently doped and

renumbered 35 and 36 (see Figure 8A-8D). The doped silicon layer is then etched into island-like semiconductor layers 11 and 12 (see [0121]), of which a portion of each are the channel region.

In re claim 25, Zhang et al does not disclose the method wherein the semiconductor device is at least one device selected from the group consisting of personal computer, video camera, a mobile computer, a goggle type display device, a DVD player, a CD player, a portable telephone, a projector. Ohtani et al. also discloses the semiconductor device could be a mobile computer (see column 1, lines 24-25). It would have been obvious to one of ordinary skill in the art at the time the invention was made to utilize the TFT of Zhang et al. in a mobile computer since TFTs allow for a display with high information content at a high speed.

In re claim 3, Zhang et al. discloses the method of forming a semiconductor film comprising silicon (31) over an insulating substrate (1), forming an oxide film (33) which protects the semiconductor film from being etched by a subsequent doping step (33) on a surface of the semiconductor film comprising silicon, doping the semiconductor film comprising silicon with impurity ions through the oxide film (see [0118]-[0119]) and forming at least one channel region comprising a portion of the doped semiconductor film (see [0121]-[0124]).

The examiner notes that the protective oxide layer, 33, of Zhang et al. is formed directly prior to the doping process of Zhang et al. and therefore is formed to protect

during the doping process. Further, Zhang et al. teaches that the acceleration voltage of the dopant is adjusted in accordance with the thickness of the protective oxide (see [0118]). The protective oxide layer of Zhang et al. will to some degree prevent etching of the surface of the semiconductor film when compared with a semiconductor film without the protective oxide formed thereon.

Zhang et al. does not disclose the method of terminating the dangling bonds on a surface of the semiconductor film with an element to be bonded with bonding energy higher than that of Si-H bonds. Ohtani et al. discloses a method of forming a chemical oxide film by ultraviolet radiation within an oxygen ambient or immersing the substrate in ozone water or hydrogen peroxide water (see column 2, lines 39-46 and column 6, lines 55-64).

Ohtani et al. does not explicitly state that the pretreatment terminates dangling bonds on a surface of the semiconductor film with an element to be bonded with bonding energy higher than that of Si-H bonds. However, as explained in the applicant's specification the termination of bonds in the present application occur with oxygen which is an with an element to be bonded with bonding energy higher than that of Si-H bonds (see specification, page 17, line 13 through page 18, line 5). The examiner points out that the amorphous silicon of Ohtani et al. is oxidized by illuminating the substrate with a UV light in an oxygen ambient to form the chemical oxide. The examiner notes that UV light increases the reactivity of the oxygen atoms, and it is clear by the formation of the oxide that the oxygen molecules react on the surface of the substrate, thus terminating the dangling bonds on the surface of the

semiconductor film with oxygen. Finally the examiner notes that Ohtani et al. discloses the same conditions and steps as that of the applicant's disclosure with respect to forming the oxide. Applicant has not provided any required conditions for forming the oxide that would terminate the dangling bonds other than the presence of oxygen.

The examiner notes that Zhang et al. does not disclose a particular method for forming the oxide layer ,33, and therefore the particular method used to form the oxide layer lacks criticality in the invention of Zhang et al. One of ordinary skill in the art at the time the invention was made would have recognized that any known method could be used to form the oxide layer, 33, in the absence of a particular suggestion by Zhang et al. Ohtani et al. discloses a method of forming a chemical oxide film (see column 2, lines 44-46, and column 6, lines 55-64) that terminates dangling bonds on a surface of the semiconductor film with an element to be bonded with bonding energy higher than that of Si-H bonds as explained above . It would have been obvious to one of ordinary skill in the art at the time the invention was made to form the oxide layer of Zhang et al. by the method of Ohtani et al., since the method of forming an oxide layer lacks criticality in the invention of Zhang et al., and since the method of Ohtani is a known method of forming an oxide that improves the surface characteristics of the underlying film (see Ohtani et al. column 2, lines 39-46).

In re claim 6, Zhang et al. teaches the method wherein the semiconductor film comprising silicon is an amorphous semiconductor film comprising silicon (31, see [0117]).

In re claims 9 and 12, Zhang et al does not disclose the method wherein a catalytic element of Ni, having the effect of accelerating crystallization is applied to the amorphous semiconductor film, and a heat treatment is conducted to form a crystalline semiconductor film. Ohtani et al. also discloses the method wherein a catalytic element of Ni, having the effect of accelerating crystallization is applied to the amorphous semiconductor film, and a heat treatment is conducted to form a crystalline semiconductor film (see column 7, lines 20-50). It would have been obvious to one of ordinary skill in the art at the time the invention was made to apply a catalytic element of Ni to the amorphous silicon film of Zhang et al. in order to accelerate the crystallization of the amorphous silicon film, thereby increasing throughput.

In re claim 15, Zhang et al. teaches the method wherein the material including hydrogen is used as the ion source for the impurity ions (see [0118]-[0119]).

In re claim 18, Zhang et al. teaches the method wherein the doping step allows channel doping to be implemented (see [0124]). The examiner notes that the doping step of [0118-0119]) creates a doped silicon layer 34, which is subsequently doped and renumbered 35 and 36 (see Figure 8A-8D). The doped silicon layer is then etched into island-like semiconductor layers 11 and 12 (see [0121]), of which a portion of each are the channel region.

In re claim 26, Zhang et al does not disclose the method wherein the semiconductor device is at least one device selected from the group consisting of personal computer, video camera, a mobile computer, a goggle type display device, a DVD player, a CD player, a portable telephone, a projector. Ohtani et al. also discloses

the semiconductor device could be a mobile computer (see column 1, lines 24-25). It would have been obvious to one of ordinary skill in the art at the time the invention was made to utilize the TFT of Zhang et al. in a mobile computer since TFTs allow for a display with high information content at a high speed.

In re claim 23, Zhang et al. discloses the method of forming a semiconductor film comprising silicon (31), specifically amorphous silicon, over an insulating substrate (1), forming a oxide film (33) on a surface of the semiconductor film comprising silicon, and doping the semiconductor film comprising silicon with impurity ions through the oxide film (see [0118]-[0119]), patterning the semiconductor film to form at least on active layer after doping (see [0121]), forming a gate insulating film (5) over the active layer after patterning the semiconductor film and forming a gate electrode (6) over the semiconductor film with the gate insulating film interposed therebetween.

Zhang et al. does not disclose the method of forming a chemical oxide film, wherein the chemical oxide film is formed by a treatment with at least one material selected form the group of ozone water and a hydrogen peroxide solution. The examiner notes that Zhang et al. does not disclose a particular method for forming the oxide layer 33, and therefore the particular method used to form the oxide layer lacks criticality in the invention of Zhang et al. One of ordinary skill in the art at the time the invention was made would have recognized that any known method could be used to form the oxide layer, 33, in the absence of a particular suggestion by Zhang et al.

Ohtani et al. discloses a method of forming a chemical oxide film wherein the chemical oxide film is formed by a treatment with at least one material selected form the group of ozone water and a hydrogen peroxide solution (see column 2, lines 44-46, and column 6, lines 55-64). It would have been obvious to one of ordinary skill in the art at the time the invention was made to form the oxide layer of Zhang et al. by the method of Ohtani et al., since the method of forming an oxide layer lacks criticality in the invention of Zhang et al., and since the method of Ohtani et al. is a known method of forming an oxide that improves the surface characteristics of the underlying film (see Ohtani et al. column 2, lines 39-46).

In re claim 27, Zhang et al does not disclose the method wherein the semiconductor device is at least one device selected from the group consisting of personal computer, video camera, a mobile computer, a goggle type display device, a DVD player, a CD player, a portable telephone, a projector. Ohtani et al. also discloses the semiconductor device could be a mobile computer (see column 1, lines 24-25). It would have been obvious to one of ordinary skill in the art at the time the invention was made to utilize the TFT of Zhang et al. in a mobile computer since TFTs allow for a display with high information content at a high speed.

In re claim 28, Zhang et al. discloses the method of forming a semiconductor film comprising silicon (31), specifically amorphous silicon, over an insulating substrate (1), forming a oxide film (33) on a surface of the semiconductor film comprising silicon, and doping the semiconductor film comprising silicon with impurity ions through the oxide

film (see [0118])-[0119]), forming a gate insulating film (5) over the semiconductor film after doping and forming a gate electrode (6) over the gate insulating film.

Zhang et al. does not disclose the method forming a chemical oxide film. The examiner notes that Zhang et al. does not disclose a particular method for forming the oxide layer, 33, and therefore the particular method used to form the oxide layer lacks criticality in the invention of Zhang et al. One of ordinary skill in the art at the time the invention was made would have recognized that any known method could be used to form the oxide layer, 33, in the absence of a particular suggestion by Zhang et al. Ohtani et al. discloses a method of forming a chemical oxide film (see column 2, lines 44-46, and column 6, lines 55-64). It would have been obvious to one of ordinary skill in the art at the time the invention was made to form the oxide layer of Zhang et al. by the method of Ohtani et al., since the method of forming an oxide layer lacks criticality in the invention of Zhang et al., and since the method of Ohtani is a known method of forming an oxide that improves the surface characteristics of the underlying film (see Ohtani et al. column 2, lines 39-46).

In re claim 31, Zhang et al. further discloses the method wherein in the doping step a material gas is at least one selected from the group consisting of diborane, phosphine, arsine and those obtained through dilution thereof with hydrogen (see [0118]-[0119]).

In re claim 34, Zhang et al does not disclose the method wherein the semiconductor device is at least one device selected from the group consisting of personal computer, video camera, a mobile computer, a goggle type display device, a

DVD player, a CD player, a portable telephone, a projector. Ohtani et al. also discloses the semiconductor device could be a mobile computer (see column 1, lines 24-25). It would have been obvious to one of ordinary skill in the art at the time the invention was made to utilize the TFT of Zhang et al. in a mobile computer since TFTs allow for a display with high information content at a high speed.

In re claim 29, Zhang et al. discloses the method of forming a semiconductor film comprising silicon (31) over an insulating substrate (1), forming an oxide film (33) which protects the semiconductor film from being etched by a subsequent doping step on a surface of the semiconductor film comprising silicon, doping the semiconductor film comprising silicon with impurity ions after forming the oxide film (see [0118]-[0119]), forming a gate insulating film (5) over the semiconductor film after doping, and forming a gate electrode (6) over the gate insulating film.

The examiner notes that the protective oxide layer, 33, of Zhang et al. is formed directly prior to the doping process of Zhang et al. and therefore is formed to protect during the doping process. Further, Zhang et al. teaches that the acceleration voltage of the dopant is adjusted in accordance with the thickness of the protective oxide (see [0118]). The protective oxide layer of Zhang et al. will to some degree prevent etching of the surface of the semiconductor film when compared with a semiconductor film without the protective oxide formed thereon.

Zhang et al. does not disclose the method of terminating the dangling bonds on a surface of the semiconductor film with oxygen. Ohtani et al. discloses a method of

forming a chemical oxide film by ultraviolet radiation within an oxygen ambient or immersing the substrate in ozone water or hydrogen peroxide water (see column 2, lines 39-46 and column 6, lines 55-64).

Ohtani et al. does not explicitly state that the pretreatment terminates dangling bonds on a surface of the semiconductor film with oxygen. However, as explained in the applicant's specification the termination of bonds in the present application occur with oxygen (see specification, page 17, line 13 through page 18, line 5). The examiner points out that the amorphous silicon of Ohtani et al. is oxidized by illuminating the substrate with a UV light in an oxygen ambient to form the chemical oxide. The examiner notes that UV light increases the reactivity of the oxygen atoms, and it is clear by the formation of the oxide that the oxygen molecules react on the surface of the substrate, thus terminating the dangling bonds on the surface of the semiconductor film with oxygen. Finally the examiner notes that Ohtani et al. discloses the same conditions and steps as that of the applicant's disclosure with respect to forming the oxide. Applicant has not provided any required conditions for forming the oxide that would terminate the dangling bonds other than the presence of oxygen.

The examiner notes that Zhang et al. does not disclose a particular method for forming the oxide layer, 33, and therefore the particular method used to form the oxide layer lacks criticality in the invention of Zhang et al. One of ordinary skill in the art at the time the invention was made would have recognized that any known method could be used to form the oxide layer, 33, in the absence of a particular suggestion by Zhang et al. Ohtani et al. discloses a method of forming a chemical oxide film (see column 2,

lines 44-46, and column 6, lines 55-64) that terminates dangling bonds with oxygen as explained above. It would have been obvious to one of ordinary skill in the art at the time the invention was made to form the oxide layer of Zhang et al. by the method of Ohtani et al., since the method of forming an oxide layer lacks criticality in the invention of Zhang et al., and since the method of Ohtani is a known method of forming an oxide that improves the surface characteristics of the underlying film (see Ohtani et al. column 2, lines 39-46).

In re claim 32, Zhang et al. further discloses the method wherein in the doping step a material gas is at least one selected from the group consisting of diborane, phosphine, arsine and those obtained through dilution thereof with hydrogen (see [0118]-[0119]).

In re claim 35, Zhang et al does not disclose the method wherein the semiconductor device is at least one device selected from the group consisting of personal computer, video camera, a mobile computer, a goggle type display device, a DVD player, a CD player, a portable telephone, a projector. Ohtani et al. also discloses the semiconductor device could be a mobile computer (see column 1, lines 24-25). It would have been obvious to one of ordinary skill in the art at the time the invention was made to utilize the TFT of Zhang et al. in a mobile computer since TFTs allow for a display with high information content at a high speed.

In re claim 30, Zhang et al. discloses the method of forming a semiconductor film comprising silicon (31) over an insulating substrate (1), forming an oxide film (33) which protects the semiconductor film from being etched by a subsequent doping step on a surface of the semiconductor film comprising silicon, doping the semiconductor film comprising silicon with impurity ions after forming the oxide film (see [0118]-[0119]), forming a gate insulating film (5) over the semiconductor film after doping, and forming a gate electrode (6) over the gate insulating film.

The examiner notes that the protective oxide layer, 33, of Zhang et al. is formed directly prior to the doping process of Zhang et al. and therefore is formed to protect during the doping process. Further, Zhang et al. teaches that the acceleration voltage of the dopant is adjusted in accordance with the thickness of the protective oxide (see [0118]). The protective oxide layer of Zhang et al. will to some degree prevent etching of the surface of the semiconductor film when compared with a semiconductor film without the protective oxide formed thereon.

Zhang et al. does not disclose the method of terminating the dangling bonds on a surface of the semiconductor film with an element to be bonded with bonding energy higher than that of Si-H bonds. Ohtani et al. discloses a method of forming a chemical oxide film by ultraviolet radiation within an oxygen ambient or immersing the substrate in ozone water or hydrogen peroxide water (see column 2, lines 39-46 and column 6, lines 55-64).

Ohtani et al. does not explicitly state that the pretreatment terminates dangling bonds on a surface of the semiconductor film with an element to be bonded with

bonding energy higher than that of Si-H bonds. However, as explained in the applicant's specification the termination of bonds in the present application occur with oxygen which is an element to be bonded with bonding energy higher than that of Si-H bonds (see specification, page 17, line 13 through page 18, line 5). The examiner points out that the amorphous silicon of Ohtani et al. is oxidized by illuminating the substrate with a UV light in an oxygen ambient to form the chemical oxide. The examiner notes that UV light increases the reactivity of the oxygen atoms, and it is clear by the formation of the oxide that the oxygen molecules react on the surface of the substrate, thus terminating the dangling bonds on the surface of the semiconductor film with oxygen. Finally the examiner notes that Ohtani et al. discloses the same conditions and steps as that of the applicant's disclosure with respect to forming the oxide. Applicant has not provided any required conditions for forming the oxide that would terminate the dangling bonds other than the presence of oxygen.

The examiner notes that Zhang et al. does not disclose a particular method for forming the oxide layer, 33, and therefore the particular method used to form the oxide layer lacks criticality in the invention of Zhang et al. One of ordinary skill in the art at the time the invention was made would have recognized that any known method could be used to form the oxide layer, 33, in the absence of a particular suggestion by Zhang et al. Ohtani et al. discloses a method of forming a chemical oxide film (see column 2, lines 44-46, and column 6, lines 55-64) that terminates dangling bonds with an element to be bonded with bonding energy higher than that of Si-H bonds as explained above. It would have been obvious to one of ordinary skill in the art at the time the invention was

made to form the oxide layer of Zhang et al. by the method of Ohtani et al., since the method of forming an oxide layer lacks criticality in the invention of Zhang et al., and since the method of Ohtani is a known method of forming an oxide that improves the surface characteristics of the underlying film (see Ohtani et al. column 2, lines 39-46).

In re claim 33, Zhang et al. further discloses the method wherein in the doping step a material gas is at least one selected from the group consisting of diborane, phosphine, arsine and those obtained through dilution thereof with hydrogen (see [0118]-[0119]).

In re claim 36, Zhang et al does not disclose the method wherein the semiconductor device is at least one device selected from the group consisting of personal computer, video camera, a mobile computer, a goggle type display device, a DVD player, a CD player, a portable telephone, a projector. Ohtani et al. also discloses the semiconductor device could be a mobile computer (see column 1, lines 24-25). It would have been obvious to one of ordinary skill in the art at the time the invention was made to utilize the TFT of Zhang et al. in a mobile computer since TFTs allow for a display with high information content at a high speed.

Response to Arguments

Applicant's arguments filed June 21, 2204have been fully considered but they are not persuasive.

In response to applicant's argument that there is no suggestion to combine the references, the examiner recognizes that obviousness can only be established by

combining or modifying the teachings of the prior art to produce the claimed invention where there is some teaching, suggestion, or motivation to do so found either in the references themselves or in the knowledge generally available to one of ordinary skill in the art. See *In re Fine*, 837 F.2d 1071, 5 USPQ2d 1596 (Fed. Cir. 1988) and *In re Jones*, 958 F.2d 347, 21 USPQ2d 1941 (Fed. Cir. 1992). The examiner points to the rejection set forth above. The examiner maintains that it would have been obvious to form the oxide film of Zhang et al. by the method of Ohtani et al. since the examiner notes that Zhang et al. does not disclose a particular method for forming the oxide layer 33, and therefore the particular method used to form the oxide layer lacks criticality in the invention of Zhang et al. One of ordinary skill in the art at the time the invention was made would have recognized that any known method could be used to form the oxide layer 33 in the absence of a particular suggestion by Zhang et al. Ohtani et al. discloses a method of forming a chemical oxide film (see column 2, lines 44-46, and column 6, lines 55-64). Thus, it would have been obvious to one of ordinary skill in the art at the time the invention was made to form the oxide layer of Zhang et al. by the method of Ohtani et al., since the method of forming an oxide layer lacks criticality in the invention of Zhang et al., and since the method of Ohtani is a known method of forming an oxide that improves the surface characteristics of the underlying film (see Ohtani et al. column 2, lines 39-46).

Applicant argues that the Official Action has not shown why one would have been motivated to remove Zhang's method of providing a protective film, and substitute instead Ohtani's method of improving surface characteristics. The examiner notes that

the combination would not be removing Zhang's method of providing a protective film of a silicon oxide. Rather, the examiner is relies upon Ohtani's method for showing how to form a silicon oxide protective film, the method having the added benefits of improving surface characteristics.

The applicant also argues that there is no discussion in the Official Action as to why one of skill in the art would have expected that the use of such thin oxide film would be successful after one has removed the protective oxide film 33 in Zhang. The examiner again notes that the combination of Zhang and Ohtani does not require removing the protective silicon oxide film of Zhang. Instead, Ohtani teaches a method of forming a protective silicon oxide that has the added benefits of improving the surface characteristics. The examiner points out that Zhang does not require a certain thickness for the protective oxide. Further, it is the method of forming the oxide that is relied upon in Ohtani, not the thickness of the layer. Since it is clear that the method of Ohtani et al. would form an oxide there is an expectation of success in the method of Zhang, which requires an oxide.

Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the

shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jennifer M. Kennedy whose telephone number is (571) 272-1672. The examiner can normally be reached on Mon.-Fri. 8:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Niebling can be reached on (571) 272-1679. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

jmk

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